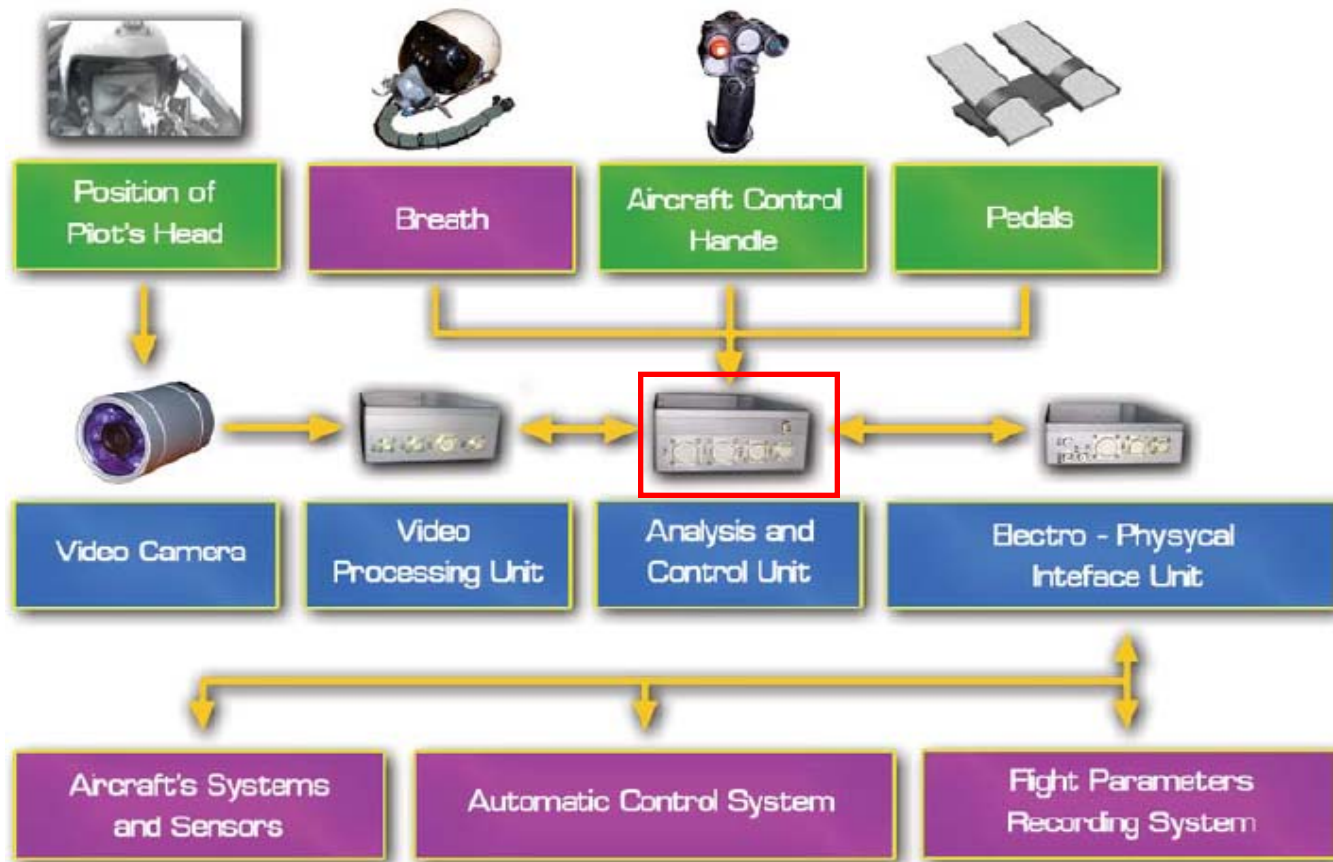

Model-Based Testing of Safety Critical Real-Time Control Logic Software

Yevgeny Gerlits, Alexey Khoroshilov

Institute for System Programming of RAS

AAFSS – Airborne Active Flight Safety System



 - AAFS sensors

 - AAFS units

 - Aircraft's embedded systems

Control Logic Software (CLS) is a subsystem

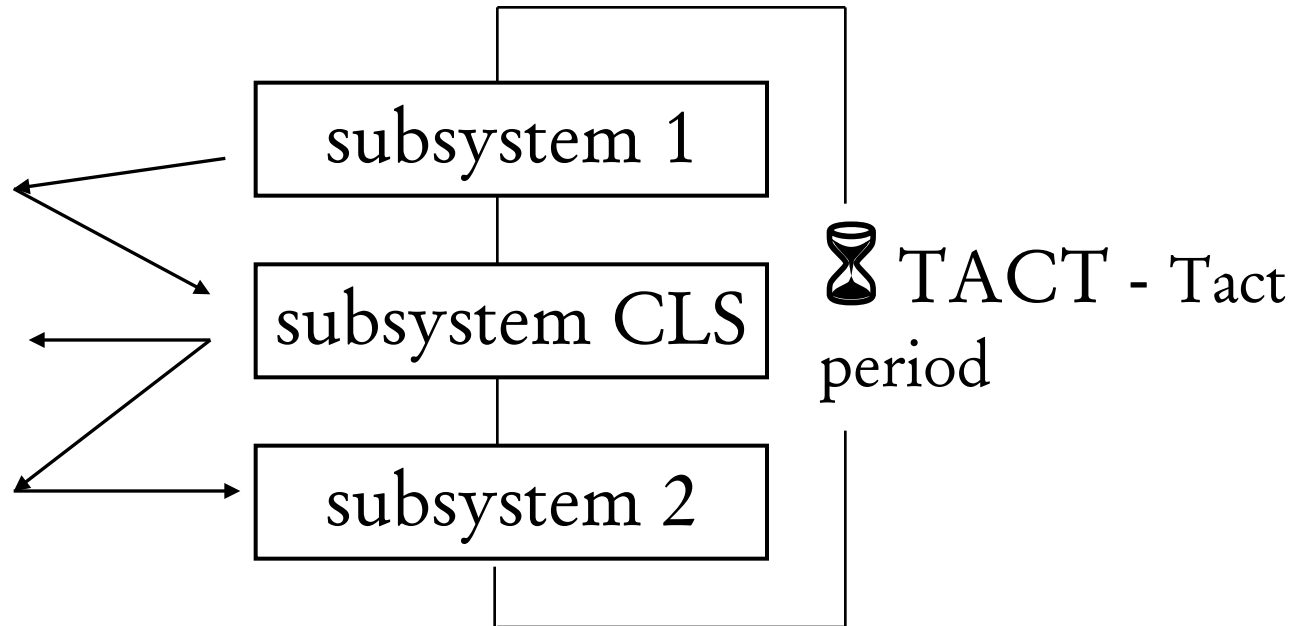
Memory pool:

sys_time

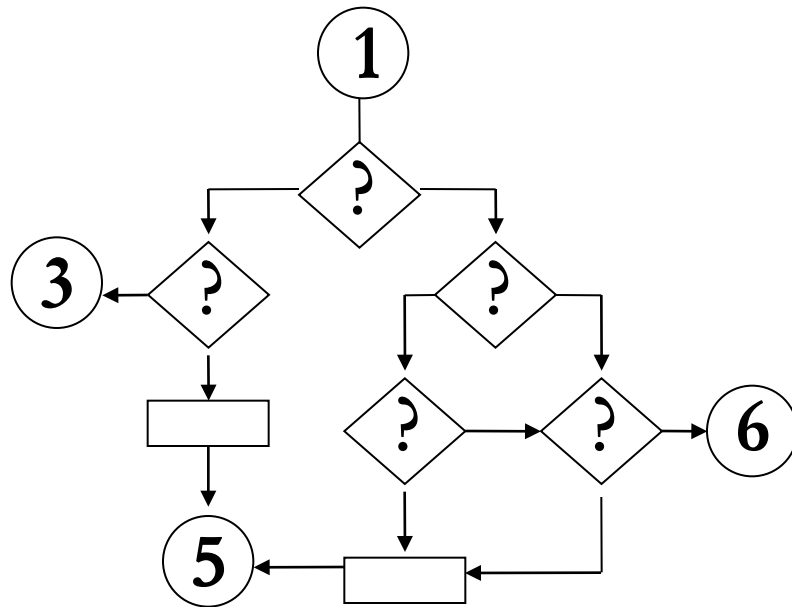
i_1, \dots, i_n

s_1, \dots, s_m

o_1, \dots, o_k



CLS is a number of decision control algorithms



① - go to algorithm j;

□ - set values of output parameters or state variables;

◇ - if condition then... else...

We consider the following conditions:

- Boolean formulas;
- $(\text{formula}(i_1, \dots, i_n), T) = \text{true}$ if Boolean formula has been true for T or more.

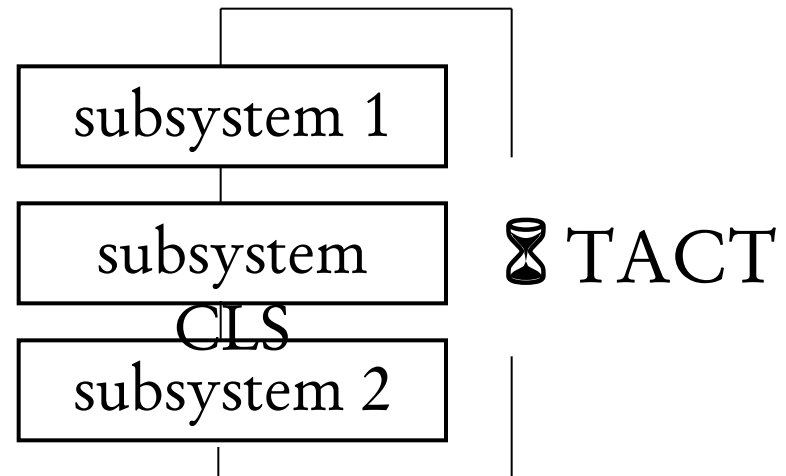
Real Time in CLS

- total time of all subsystems execution $<$ tact period;
- temporal condition $(\text{formula}(i_1, \dots, i_n), T) = \text{true}$ if Boolean formula has been true for T or more:

Example: $(i_1 < 5, 2 * \text{TACT})$

Turns of the global control loop:

1. $\text{sys_time} = 0; i_1 = 3;$
2. $\text{sys_time} = \text{TACT}; i_1 = 2;$
3. $\text{sys_time} = 2 * \text{TACT}; i_1 = 3;$
4. $\text{sys_time} = 3 * \text{TACT}; i_1 = 6.$



Temporal condition is closer to state than to real time

How does CLS calculate $(\text{formula}(i_1, \dots, i_n), T)$?

- Let sys_time_f be sys_time since when $\text{formula}(i_1, \dots, i_n)$ has been TRUE;
- $(\text{formula}(i_1, \dots, i_n), T) = \text{formula}(i_1, \dots, i_n) \ \&\& \ (\text{sys_time} - \text{sys_time}_f) \geq T.$

Characteristics of CLS

- ~ 2 000 lines of code;
- Low Level Requirements are 9 flow charts of size A4;
- 32 input parameters of different types;
- 7 state variables of different types;
- 80 temporal conditions in branch instructions;
- 9 output parameters;
- tact period is 60 ms.

Problem Definition

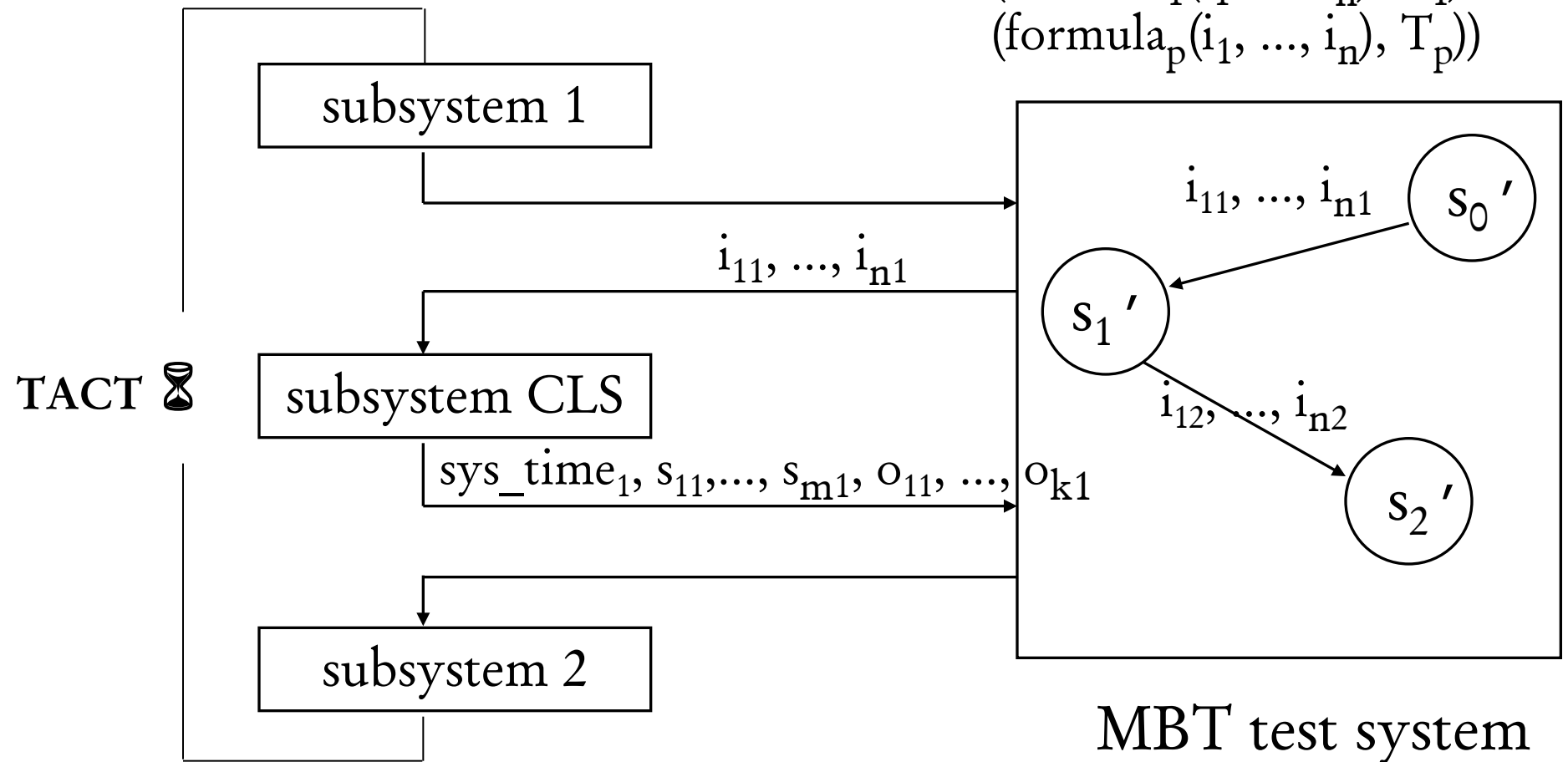
- huge number of input parameters (32);
- huge space of states (7 state variables + 80 temporal conditions);
- CLS is a safety critical software (MC/DC metric)
⇒ Traditional unit testing doesn't work well.

- real time characteristics of CLS are not complicated
⇒ Real Time specific MBT approaches (UPAAL Tron, Timed TorX) are not ultimately required.

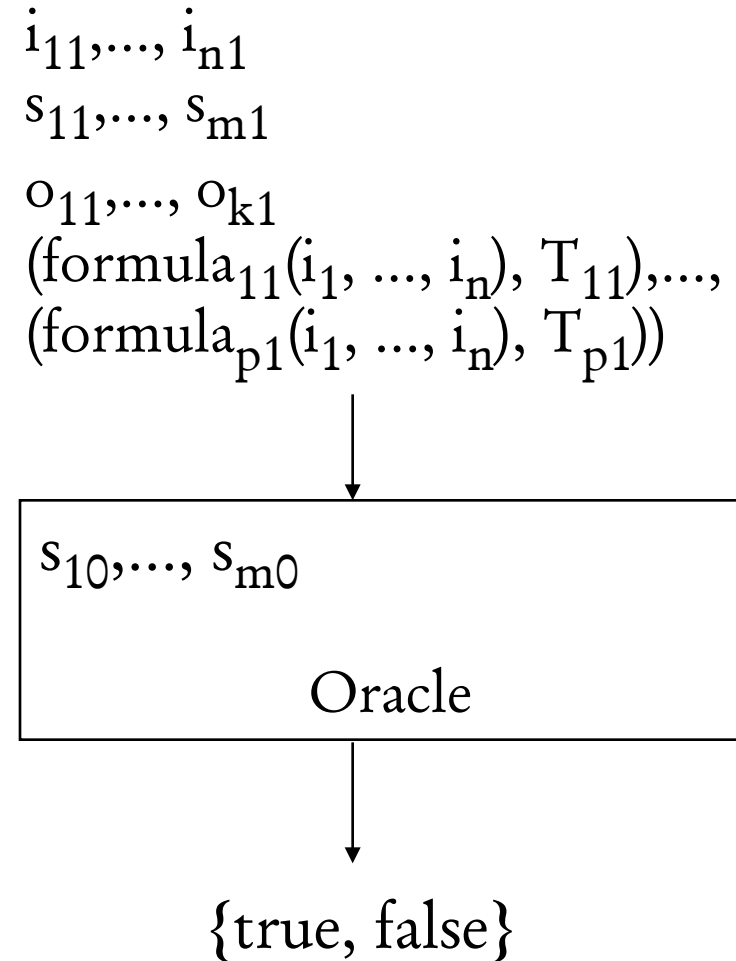
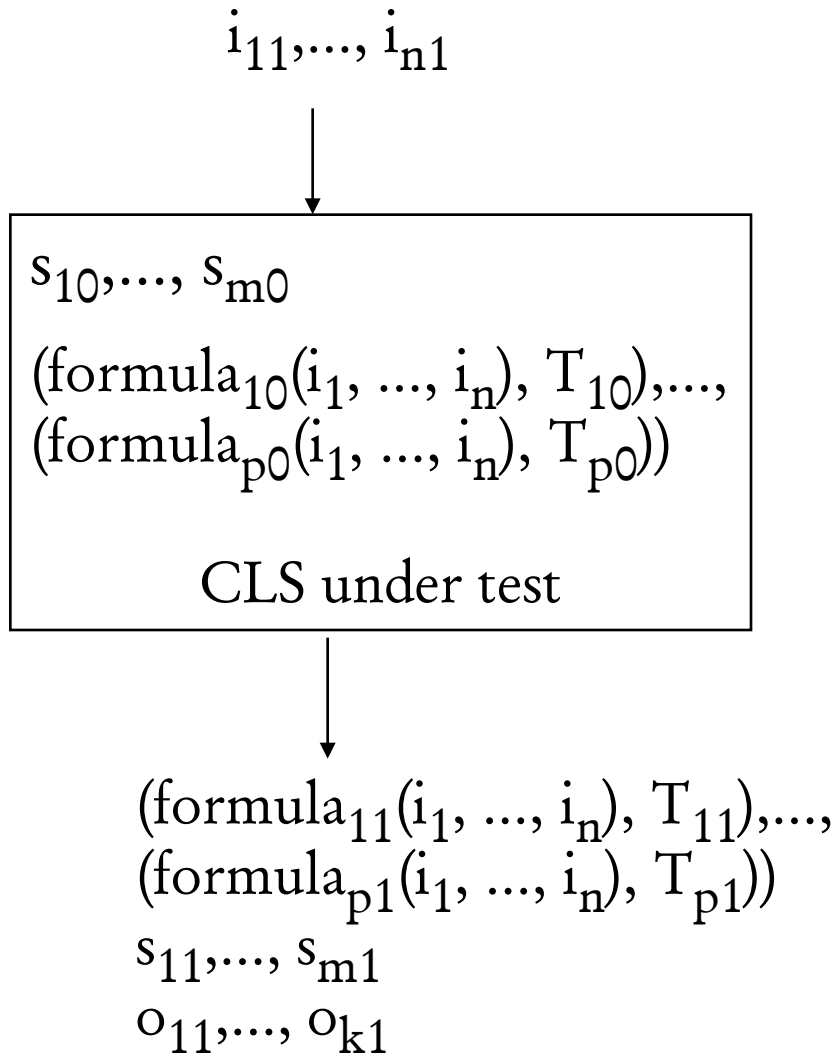
- industrial tool is required in a real project
⇒ try a general purpose MBT (SpecExplorer, UniTESK).

On the Fly MBT Approach

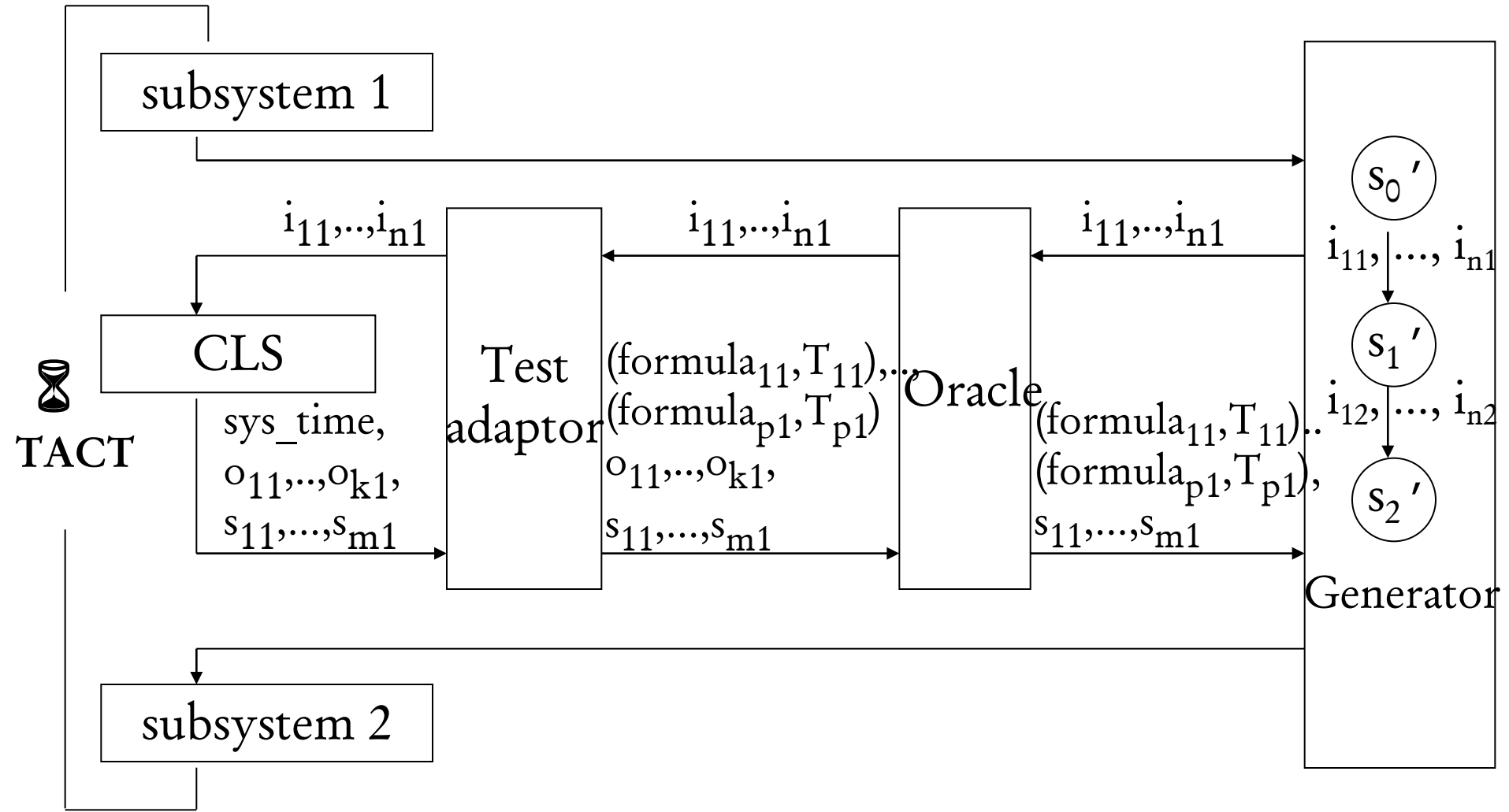
$$s' = \text{genState} (s_1, \dots, s_m, \\ (\text{formula}_1(i_1, \dots, i_n), T_1), \dots, \\ (\text{formula}_p(i_1, \dots, i_n), T_p))$$



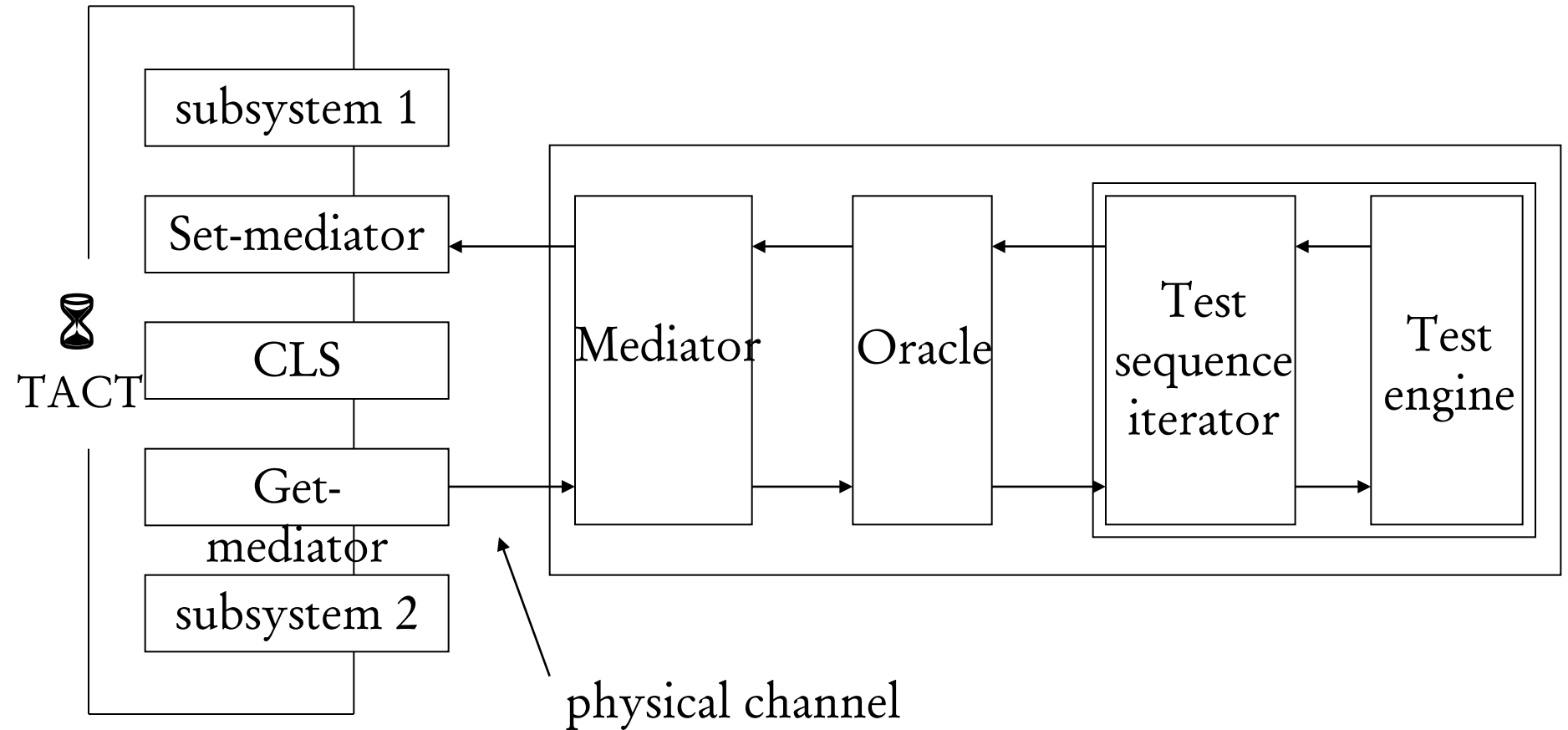
Oracle



MBT Scheme



UniTESK MBT Scheme



Target computer system (device)

Host computer system (Server)

Conclusion

1. The RTCLS subsystem and the architecture of the whole embedded device were described;
 2. An MBT approach to RTCLS was outlined in general terms;
 3. The MBT approach was implemented using UniTESK.
- ⇒ general purpose MBT like UniTESK are applicable to CLS.

Thank you! Questions?